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TITLE: Data processor device for multiprocessor system - has second data inputs-outputs of second commutator connected to second data inputs-outputs of command register

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PRIORITY-DATA: 1988SU-4653165 (November 24, 1988)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
SU 1683039 A1	October 7, 1991	N/A	003	G06F 015/76

APPLICATION-DATA:

PUB-NO	APPL-DESCRIPTOR	APPL-NO	APPL-DATE
SU 1683039A1	N/A	1988SU-4653165	November 24, 1988

INT-CL (IPC): G06F015/16, G06F015/76

ABSTRACTED-PUB-NO: SU 1683039A

BASIC-ABSTRACT:

The data processing system which includes a processor (1), local memory (5) and address register (11), for faster response, now incorporates a communication memory (6), first and second commutators (7,8), command register (9), data register (10) and control unit (12). The input-outputs, address (data) and control of the processor are connected via the local common bus to the analogous inputs-outputs of the local memory (5), the first inputs-outputs of the first commutator (7), to the first data inputs-outputs of the command register (9) and to the first inputs-outputs of the second commutator (8).

Prior to the start of data exchange the control processor (1.1) loads the address registers (11.i) (i=1,n) of n processors taking the part in data exchange by the initial addresses of the memory areas, the contents of which should be transmitted.

USE/ADVANTAGE - For designing multiprocessor systems. Provision for combining calculation with data exchange. Bul.37/7.10.91.

CHOSEN-DRAWING: Dwg.1/1

TITLE-TERMS: DATA PROCESSOR DEVICE MULTIPROCESSOR SYSTEM SECOND DATA INPUT  
OUTPUT SECOND COMMUTATE CONNECT SECOND DATA INPUT OUTPUT COMMAND  
REGISTER

DERWENT-CLASS: T01

EPI-CODES: T01-H08; T01-M02;

SECONDARY-ACC-NO:

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